Job advertisement form

EMPLOYER'S DETAILS

Company/Employer name*:	**Nokia Solutions and Networks Oy, Oulu**
Address*:	**Kaapelitie 4** **90620 Oulu, Finland**
Telephone:	
Website:	https://www.nokia.com/ and https://careers.nokia.com/

Description of the employer*:	Nokia is a global leader in the technologies that connect people and things. With state-of-the-art software, hardware and services for any type of network, Nokia is uniquely positioned to help communication service providers, governments, and large enterprises deliver on the promise of 5G, the Cloud and the Internet of Things. Serving customers in over 100 countries, our research scientists and engineers continue to invent and accelerate new technologies that will increasingly transform the way people and things communicate and connect. Imagine creating technology that has the potential to change the world. With us, you will work with next-generation technology to design products and solutions that will shape the future.

DETAILS AND DESCRIPTION OF THE POSITION

Eures portal ref:	NTFlZGVlMzAtMWNhMC00YTA3LTk5NWItZmQ4M2Q3Y2E0YjE1IDgx
Job title*:	**SoC Verification Engineer (in ISCO: 21520 + 21530, 21531)**
Work experience (none – 1 y, 1 – 5 yrs, more than 5 yrs):	**over 5 years**
Number of employees to be recruited*:	**8**
Initial date of employment (date or text)*:	**according to agreement**
Duration of employment (less than 11 days, 11 days – 1 mth, 1 – 3 mths, 3 – 6 mths, 6 – 12 mths, more than 12 mths)*:	**more than 12 months (permanent)**
Working time (full-time/shift work/part-time/night work/weekend work) and number of hours*:	**Full-time**
Salary: (gross or net) (hour/mth/year)	**according to agreement**
Type of employment contract (paid work, commission-based, entrepreneur)*:	**paid work**

Job description requirements etc*:

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Job Description

We are looking for an engineer with a specialist level of experience in RTL verification activities. An engineer who develops tests, simulates, analyzes, and is interested in debugging our SoC or IP design and architectures, e.g. in Digital Radio Front End (DFE) area. A person willing and capable of leading, supporting, teaching and coaching less experienced verification engineers. A person who can act as verification lead in our IP/SoC top-level activities.

You have a hungry mind to learn more and you are excited doing a variety of different kinds of design and verification tasks as part of your job. You are always ready to share your knowledge and skills among the people you work with. You are a team worker but also efficient on independent tasks, and your English language skills allow you communicate efficiently in our multicultural environment.

Main Responsibility Area

As a member of SoC DU organization you will participate to RTL verification within your responsibility area. You will also have the responsibilities in: specifying features and creating verification plans, developing test environments, creating test cases, running simulations, analyzing and debugging the design, reviewing documents, and creating verification documentation for a SoC/IP block area.

Job Description:

- Participate in outlining System-on-Chip or IP verification activities
- Preparation and review of related verification documents and review of functional and design specification for SoC/IP
- RTL verification environment and VIP development
- Test case development, running regressions and debugging
- Prepares and reviews System-on-Chip development documentations and application documents
- Cooperate with system engineers, HW/SW development, suppliers and other relevant functions to solve technical issues for quality.
- Supporting other RTL verification, integration and prototyping teams
- Support HW/SW Bring-up and debug

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Additional Requirements:

- Master's Degree in Engineering or equivalent
- Over 6 years working experience in RTL verification
- Good understanding of digital signal processing and related mathematics
- Specialist with SystemVerilog and UVM usage, many years of coding and verification experience
- Understanding VHDL/Verilog language and design debugging methods
- Very good knowledge of SoC (ASIC/FPGA/low level SW) design and verification tools.
- Basic understanding of Cellular networks and 2G, 3G, LTE and 5G technology
- Fluent spoken and written English

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additional information	**for senior consultants a relocation package is possible**
This employment complies with the legislation of FINLAND *	
Contact	**https://careers.nokia.com/**

APPLICATION PROCESS

Deadline for applications*:	**17th September 2021**
Send applications to:	**https://aluperf.referrals.selectminds.com/jobs/soc-verification-engineer-60598**