Job advertisement form

EMPLOYER'S DETAILS

Company/Employer name*:	**Nokia Solutions and Networks Oy, Oulu**
Address*:	**Kaapelitie 4** **90620 Oulu, Finland**
Telephone:	
Website:	https://www.nokia.com/ and https://careers.nokia.com/

Description of the employer*:	Nokia is a global leader in the technologies that connect people and things. With state-of-the-art software, hardware and services for any type of network, Nokia is uniquely positioned to help communication service providers, governments, and large enterprises deliver on the promise of 5G, the Cloud and the Internet of Things. Serving customers in over 100 countries, our research scientists and engineers continue to invent and accelerate new technologies that will increasingly transform the way people and things communicate and connect. Imagine creating technology that has the potential to change the world. With us, you will work with next-generation technology to design products and solutions that will shape the future.

DETAILS AND DESCRIPTION OF THE POSITION

Eures portal ref:	Y2ViMDkyNTAtOTc0NS00ZjI1LTlkZmEtYzM2ZDE5MmRhZTA2IDgx
Job title*:	**Design Engineer, DPD IP (in ISCO: 21520 + 21530, 21531)**
Work experience (none – 1 y, 1 – 5 yrs, more than 5 yrs):	**over 5 years**
Number of employees to be recruited*:	**8**
Initial date of employment (date or text)*:	**according to agreement**
Duration of employment (less than 11 days, 11 days – 1 mth, 1 – 3 mths, 3 – 6 mths, 6 – 12 mths, more than 12 mths)*:	**more than 12 months (permanent)**
Working time (full-time/shift work/part-time/night work/weekend work) and number of hours*:	**Full-time**
Salary: (gross or net) (hour/mth/year)	**according to agreement**
Type of employment contract (paid work, commission-based, entrepreneur)*:	**paid work**

Job description requirements etc*:

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Engineer SoC/IP ASIC DESIGN

Description

Nokia is a global leader in the technologies that connect people and things. With state-of-the-art software, hardware and services for any type of network, Nokia is uniquely positioned to help communication service providers, governments, and large enterprises deliver on the promise of 5G, the Cloud and the Internet of Things. Serving customers in over 100 countries, our research scientists and engineers continue to invent and accelerate new technologies that will increasingly transform the way people and things communicate and connect.

We are looking for verification Engineer to SoC DU organization

Qualifications

We are looking for an engineer interested in RTL design and verification activities in 5G RF PA Digital Pre-Distortion (DPD) area. You have a hungry mind to learn more and you are excited doing a variety of different kinds of design and verification tasks as part of your job. You are always ready to share your knowledge and skills among the people you work with. You are a team worker but also efficient on independent tasks, and your English language skills allow you communicate efficiently in our multicultural environment.

Main Responsibility Area

Depending on your skills, capacity and willingness to take responsibility the role can be tailored to fit to you and there is always a path to extend the role to highly valued expert or technical lead role in the future.
ASIC Design engineer creates specification, implements RTL by using VHDL/Verilog, simulates the design and documents the implementation.

Job Description:

- Experience on RTL design (VHDL/VERILOG/HIGH LEVEL languages) and verification valued, role can be tailored based on skills and experience
- Does preliminary synthesis for re-usable IP blocks
- Converge algorithm system model to HW reference model for verification purposes
- Preparation and review of related design and verification documents and review of functional and design specification for SoC/IP
- Development of RTL verification environment or using available environment
- Test case development, running regressions and debugging
- Prepares and reviews System-on-Chip development documentations and application documents
- Cooperate with system engineers, HW/SW development, silicon suppliers and other relevant functions to solve technical issues for quality.
- Supporting other RTL verification, integration and prototyping teams
- Support HW/SW Bring-up and debug

Additional Requirements:

- Master's Degree in Engineering or equivalent
- Good understanding of digital signal processing and related mathematics
- Familiar with SystemVerilog and VHDL/Verilog, and some coding and verification experience
- Knowledge of SoC (ASIC/FPGA/low level SW) design and verification tools.
- Knowledge of modelling tools and methods (Matlab, Simulink, C)
- Basic understanding of Cellular networks and 2G, 3G, LTE and 5G technology
- Fluent spoken and written English

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additional information	**for senior consultants a relocation package is possible**
This employment complies with the legislation of FINLAND *	
Contact	**https://careers.nokia.com/**

APPLICATION PROCESS

Deadline for applications*:	**17th September 2021**
Send applications to:	**https://aluperf.referrals.selectminds.com/jobs/design-engineer-dpd-ip-63469**